

FIG. 1
BACKGROUND ART

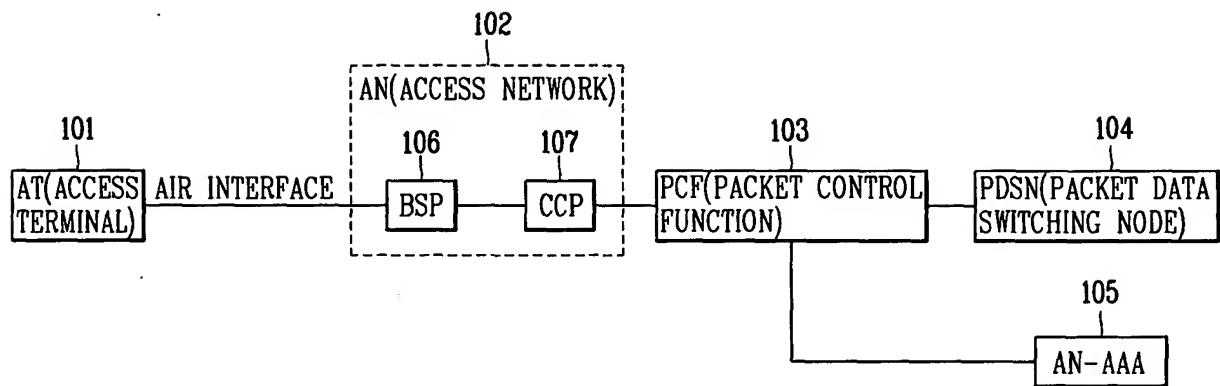


FIG. 2
BACKGROUND ART

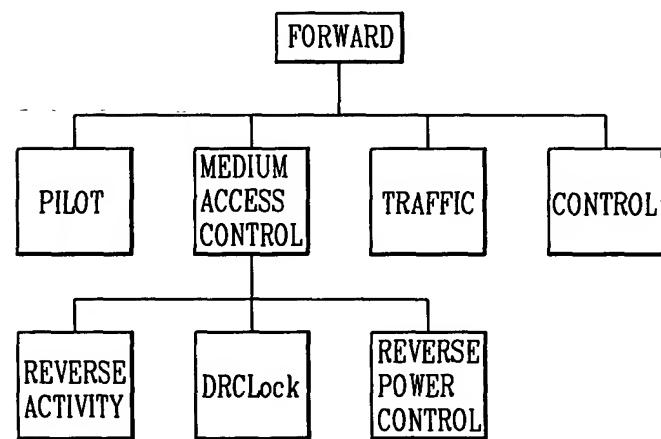


FIG. 3
BACKGROUND ART

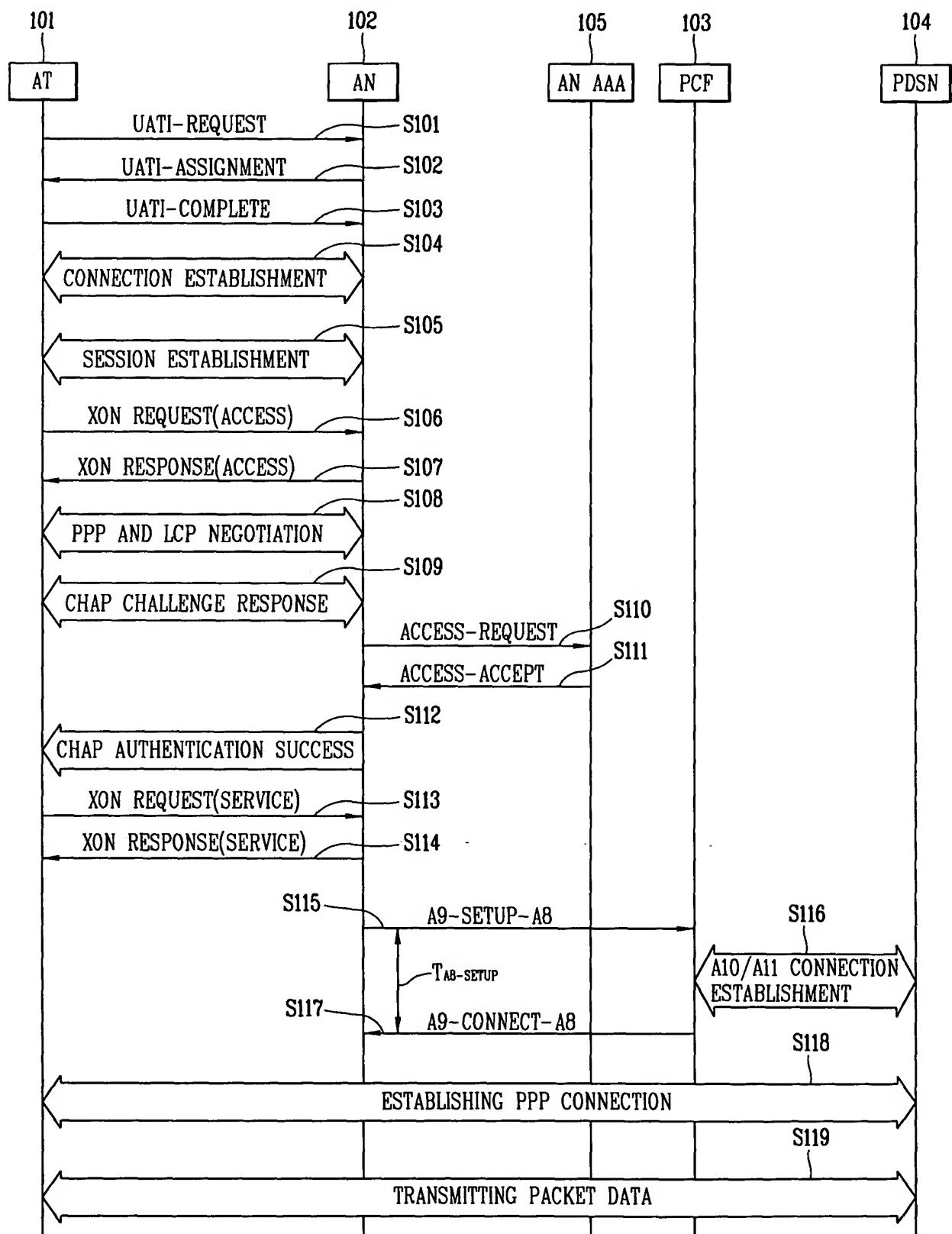


FIG. 4
BACKGROUND ART

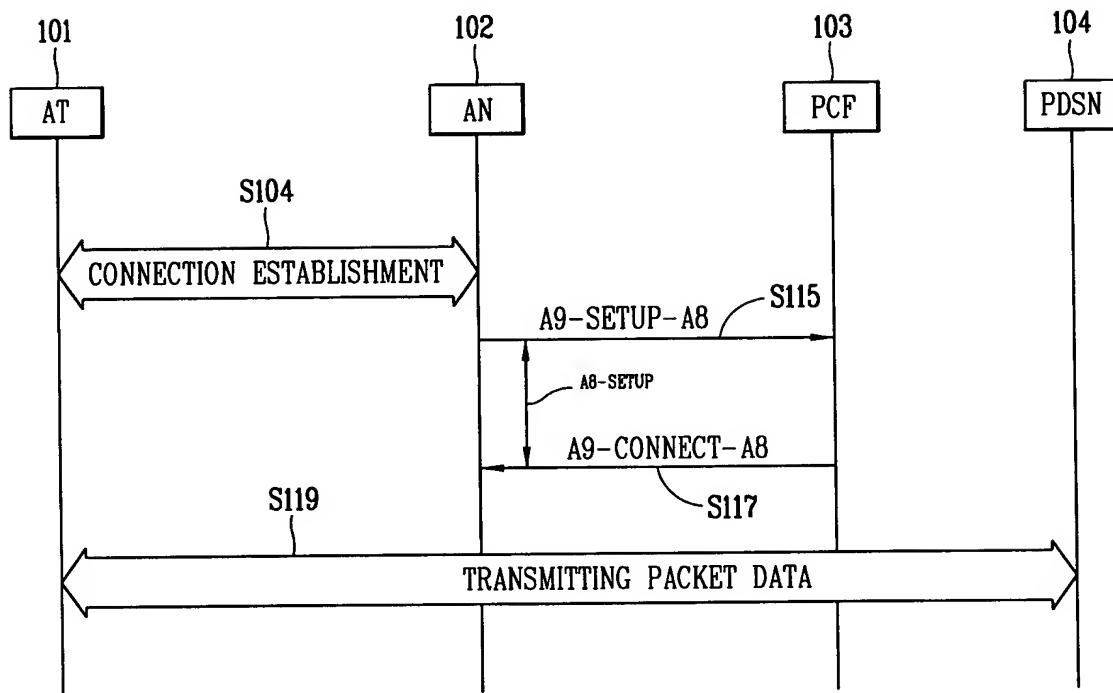


FIG. 5A

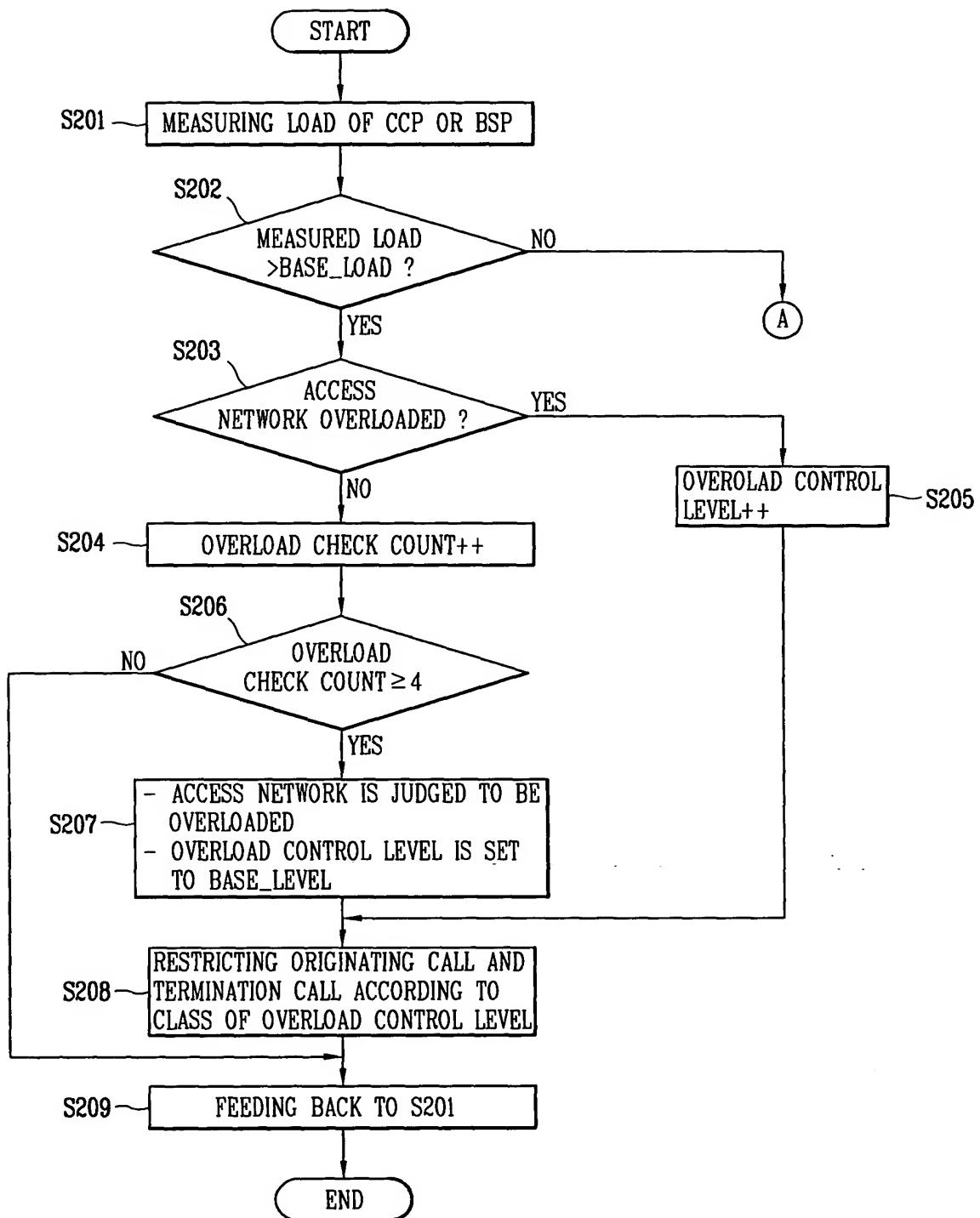


FIG. 5B

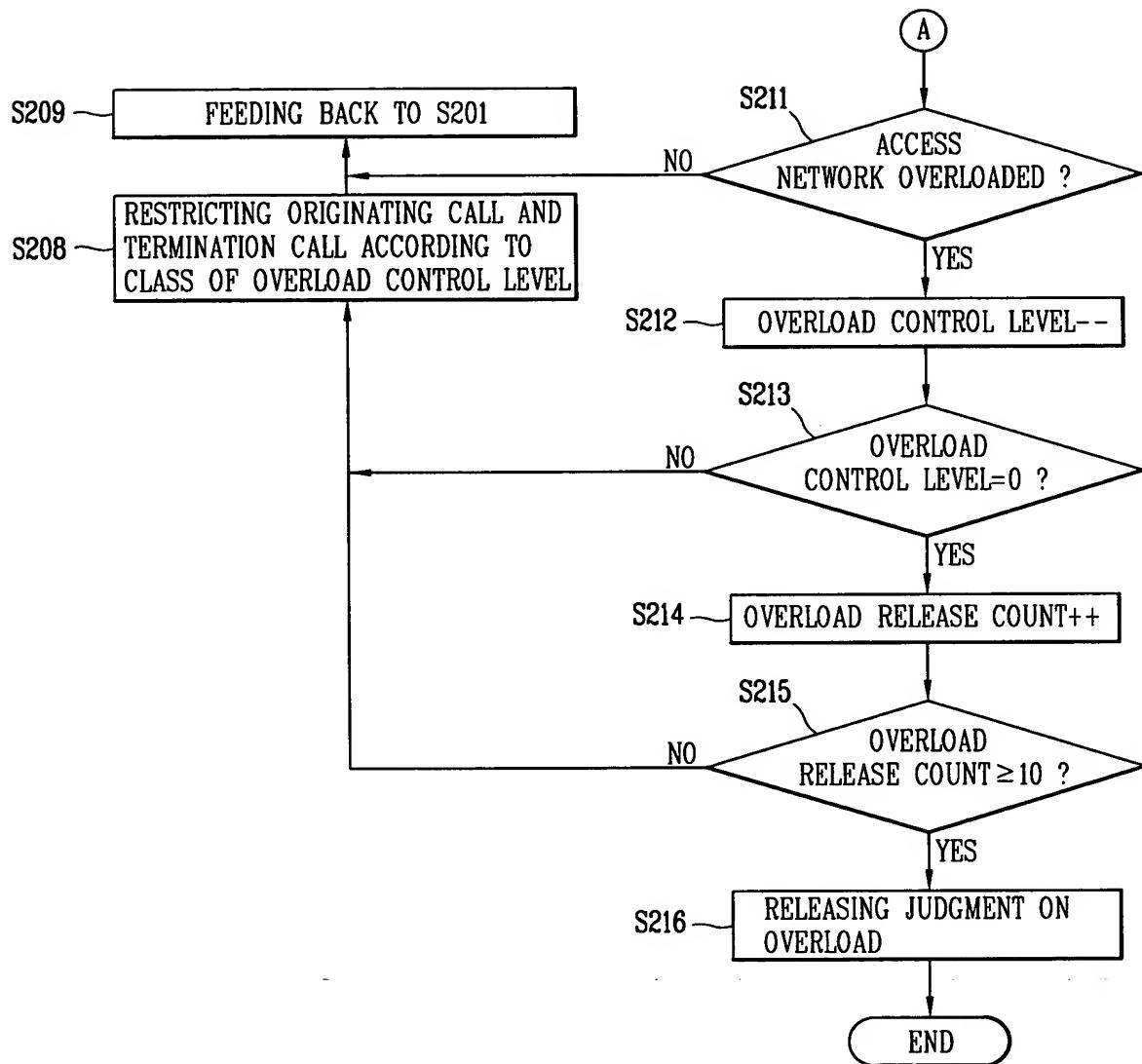


FIG. 6

OVERLOAD CLASS	OVERLOAD CONTROL LEVEL(L)	BSP PROCESSOR	CCP PROCESSOR	CONTROL SUBJECT
		ORIGINATING CALL ACCEPTANCE RATE P(%)	TERMINATION CALL ACCEPATANCE RATE P(%)	
NORMAL	0	100%	100%	
MINOR	1	84%	100%	LOWER PROCESSOR (ORIGINATING CALL CONTROL CLASS)
	2	71%	100%	
	3	60%	100%	
	4	50%	100%	
	5	42%	100%	
	6	35%	100%	
	7	30%	100%	
	8	25%	100%	
MAJOR	9	18%	100%	UPPER PROCESSOR (TERMINATION CALL CONTROL CLASS)
	10	13%	100%	
	11	5%	100%	
	12	0%	100%	
	13	0%	84%	
	14	0%	71%	
	15	0%	60%	
	16	0%	50%	
CRITICAL	17	0%	42%	
	18	0%	35%	
	19	0%	30%	
	20	0%	25%	
	21	0%	18%	
	22	0%	13%	
	23	0%	5%	
	24	0%	0%	

FIG. 7

FIELD	LENGTH(bits)
MessageID	8
AccessCycleDuration	8
AccessSignature	16
OpenLoopAdjust	8
ProbeInitialAdjust	5
ProbeNumStep	4
PowerStep	4
PreambleLength	3
CapsuleLengthMax	4
APersistence	6
Reserved	Variable

FIG. 8

OVERLOAD CLASS	OVERLOAD CONTROL LEVEL(L)	BSP PROCESSOR	APersistence VALUE	PERSISTENCE PROBABILITY P(%)
		ORIGINATING CALL ACCEPTANCE RATE P(%)		
NORMAL	0	100%	0x0	100%
MINOR	1	84%	0x1	84%
	2	71%	0x2	71%
	3	60%	0x3	60%
	4	50%	0x4	50%
	5	42%	0x5	42%
	6	35%	0x6	35%
	7	30%	0x7	30%
	8	25%	0x8	25%
MAJOR	9	18%	0xA	18%
	10	13%	0xC	13%
	11	5%	0x11	5%
	12	0%	0x3F	0%
	13	0%	0x3F	0%
	14	0%	0x3F	0%
	15	0%	0x3F	0%
	16	0%	0x3F	0%
CRITICAL	17	0%	0x3F	0%
	18	0%	0x3F	0%
	19	0%	0x3F	0%
	20	0%	0x3F	0%
	21	0%	0x3F	0%
	22	0%	0x3F	0%
	23	0%	0x3F	0%
	24	0%	0x3F	0%